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a plurality of conduction sections each defining at least part of an external terminal with substantially rectangular contour lines, the conduction sections being electrically connected to the leads.

Sub
F2

7. (Twice Amended) A semiconductor apparatus, comprising:

a semiconductor device having a plurality of electrodes;

a substrate main body;

a plurality of leads formed on the substrate main body, the plurality of leads

radially extending from a peripheral area toward a central area of the substrate main body; and

a plurality of conduction sections formed on the substrate main body with substantially rectangular contour lines, the conduction sections being electrically connected to the leads, and one of the conduction sections defining an external terminal.

D2

REMARKS

Claims 1-4, 6-12 and 19-25 are pending. By this Amendment, claims 1 and 7 are amended merely for clarity. Reconsideration based on the above amendments and following remarks is respectfully requested.

The attached Appendix includes marked-up copies of each rewritten claim (37 C.F.R. §1.121(c)(1)(ii)).

Applicant appreciates the courtesy extended to Applicant's representative by Examiner Graybill during the July 19, 2001 personal interview. The points discussed are incorporated into the following remarks.

During the personal interview, Examiner Graybill asserted that independent apparatus claims 1 and 7 did not clearly define a plurality of conduction sections. Claims 1 and 7 are amended to further clarify the invention.